UNIVERSITY OF KERALA

TIME TABLE FOR FIFTH SEMESTER BTECH DEGREE PRACTICAL EXAMINATION FOR ELECTRONICS & COMMUNICATION ENGINEERING, SEPTEMBER 2023 2020 SCHEME UNIVERSITY COLLEGE OF ENGINEERING KARIAVATTOM (REGULAR-2020 ADMISSION)

Course	Subject	Date	Centre
	ECL 331 - Analog Integrated Circuits and Simulation Lab		University College of Engineering, Kariavattom
	ECL 333 - Digital Signal Processing Lab	10.10.2023 FN	

Thiruvananthapuram Dated 30.09.2023

Sd/-Controller of Examinations